

FORM PTO-1449	Docket No.: 2003P52600US	Serial No.: Pending
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	Applicant: Jungwon Suh	
	Filing Date: Herewith	Group Art: Unassigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
DBK	AA	6,038,382	Mar. 14, 2000	Kanazawa		
DBK	AB	6,519,201	Feb. 11, 2003	Cowles et al.		
DBK	AC	6,549,476	Apr. 15, 2003	Pinney		
DBK	AD	6,556,497	Apr. 29, 2003	Cowles et al.		
DBK	AE	6,567,287	May 20, 2003	Scheuerlein		
DBK	AF	6,586,300	Jul. 1, 2003	Hummeler et al.		
DBK	AG	6,603,694	Aug. 5, 2003	Frankowsky et al.		
	AH					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translated Yes No
AI						Yes
AJ						Yes

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

DBK	AK	Ars Technica, LLC webpage entitled "RAM Guide: Part I: DRAM and SRAM Basics – Storage Theory", 5 pgs; available at www.arstechnica.com , Jon "Hannibal" Stokes, © 1998-2003.
DBK	AL	Ars Technica, LLC webpage entitled "RAM Guide: Part I: DRAM and SRAM Basics – RAM Chips", 4 pgs; available at www.arstechnica.com , Jon "Hannibal" Stokes, © 1998-2003.
DBK	AM	Ars Technica, LLC webpage entitled "RAM Guide: Part I: DRAM and SRAM Basics – RAM chips", 6 pgs; available at www.arstechnica.com , Jon "Hannibal" Stokes, © 1998-2003.
DBK	AN	Ars Technica, LLC webpage entitled "Ars Technica RAM Guide, Part II: Asynchronous and Synchronous DRAM", 5 pgs; available at www.arstechnica.com , Jon "Hannibal" Stokes, © 1998-2003.
DBK	AO	Ars Technica, LLC webpage entitled "Ars Technica RAM Guide, Part III: DDR DRAM and RAMBUS", 5 pgs; available at www.arstechnica.com , Jon "Hannibal" Stokes, © 1998-2003.

EXAMINER:

Daniel Ho

DATE CONSIDERED:

10/29/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.